

**ARM® Cortex®-M0**  
**32-bit Microcontroller**

**NuMicro® Family**  
**M071R/M071S Series BSP**  
**Revision History**

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### Revision 3.00.002 (Released 2023-05-19)

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1. Modify VerdorName of Nu\_DFU.inf.
2. Set FlashSelect of ISP\_DFU sample code to LDROM.
3. Fix SPI\_Loopback GCC sample code issue.
4. Fix I2C\_EEPROM and I2C\_GCMode failed in GCC.
5. Modify startup\_M071M.s in IAR/ARM file.
6. Modify sample code for keil compiler 6.
7. Add SYS\_PowerDown\_MinCurrent sample code.
8. Update SPI\_Loopback sample code.

### Revision 3.00.001 (Released 2020-07-10)

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1. Initial Release.

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